

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	16576	wiring with (hole\$1 opening\$1) with (insulating dielectric)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/18 16:24
L2	496	1 same parallel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/18 16:24
L3	356	1 same parallel with wiring\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/18 16:25
L4	47	3 and (tft thin adj film adj transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/18 16:25